

## ABSTRACT

A method and system for performing a two-step read on a count register as an atomic read in a home network is disclosed. The count register has a lower half and an upper half, and is incremented in response to an increment signal and is read in response to a read signal. The method and system include reading the lower half of the count register and storing the upper half of the count register in a shadow register in response to detecting the increment signal. The shadow register is then read in order to obtain the value of the upper half of the count register, thereby avoiding reading an incorrect result from the register.

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